Processing Power Requirements

	CPU (MIPS) In 1997	CPU (MIPS) In 2006
Triggering		
Level-2		4×10^5
Level-3		1×10^6
Reconstruction		
Real data		1.6×10^6
Simulated data	200	2×10^5
Simulation		
B-meson	600	1.2×10^6
Background	-	1.2×10^3
Analysis		
Real data	-	5×10^4
Simulated data	150	5×10^5
Total	950	$\sim 5 \times 10^6$