

**40 MHz bunch crossing rate**



**L0 Hardware Trigger : 1 MHz readout, high  $E_T/P_T$  signatures**

<b>450 kHz</b> $h^\pm$	<b>400 kHz</b> $\mu/\mu\mu$	<b>150 kHz</b> $e/\gamma$
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**Software High Level Trigger**  
**29000 Logical CPU cores**  
**Offline reconstruction tuned to trigger time constraints**  
**Mixture of exclusive and inclusive selection algorithms**



**5 kHz Rate to storage**

<b>2 kHz</b> <b>Inclusive</b> <b>Topological</b>	<b>2 kHz</b> <b>Inclusive/</b> <b>Exclusive</b> <b>Charm</b>	<b>1 kHz</b> <b>Muon and</b> <b>DiMuon</b>
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